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\*\* CONTINUING DATA \*\*\*\*\*

*None pr*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-284886 09/19/2001

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> met after Allowance				
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials		

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## TITLE

Method of mounting electronic component on substrate without generation of voids in bonding material

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